

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al. Art Unit : 2811
Serial No. : 09/814,255 Examiner : Ori Nadav
Filed : March 21, 2001 Conf. No. : 5578
Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR INITIALED FORMS PTO-1449

Upon reviewing the application file, applicants noticed that two Form PTO-892 forms were submitted with the application on March 21, 2001. The PTO-892 forms listed references cited in the parent application (Serial Number 08/769,371). Applicants note that, pursuant to the requirements of MPEP § 609.02(A)(2), the Examiner is already obligated to consider these references when examining the present application, and, therefore, applicants cited the references at the time of the filing of the present application solely for the purpose of having the references appear on the face of any patent that issues from the present application. The Examiner has not yet signed off on these previously cited references, and, therefore, applicants submit the attached PTO-1449 forms and respectfully request that the Examiner initial and sign off on the listed references.

Applicants also noticed upon review of the application file that the Office Action mailed on March 31, 2003 for the present application included a 37 CFR 1.501 IDS attachment directed to an unrelated patent (i.e., Patent Number 5,994,221). Applicants believe that inclusion of this attachment with the Office Action of March 31, 2003 was an unintentional error by the Patent Office, and, therefore, wish to make the record clear that the attachment bears no relationship to the present application.

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0107002 / US3194/3205/3215D1

No fees are believed due in connection with this filing. However, if any fees or refunds are due, please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,



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Date: July 23, 2009

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